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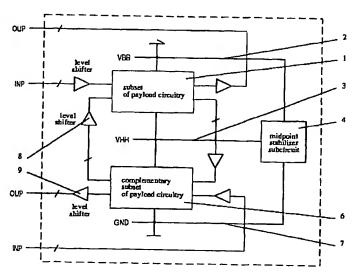
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(54) Title: LOW-VOLTAGE IC-CIRCUIT



(1,6) and where ground voltage level (VHH) in the power supply of a first sub-circuit (1) is used as the supply voltage level in a second sub-circuit (6). According to the invention a voltage control circuit (4) comprises a first buffer capacitor (10) coupled in parallel over the supply voltage level (VBB) and ground voltage level (VHH) of the first sub-circuit (1) and a second buffer capacitor (11) coupled in parallel over the supply voltage level (VHH) and the ground voltage level (GND) of the second sub-circuit (6), whereby means for (20,21,22) which is alternately coupled in parallel over the first (10) and the second (11) buffer capacitor comprises at least one bucket capacitor controlled by a toggling signal.



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